

ABSTRACT OF THE DISCLOSURE

To reduce power consumption and operation frequency, a
5 speech synthesizer comprises a volume control unit to receive a
control signal and generate a control bias, a signal transform unit to
receive the control bias V_{bias} and a series of digital speech codes
and under the control of the most significant bit of the digital speech
codes to transform the other bits of the digital speech codes or its
10 inverse into an analog signal, and a current output unit to receive
the analog signal from the signal transform unit and output the
analog signal from its two current output terminals under the
control of the most significant bit of the digital speech codes. The
speech synthesizer therefore needs no additional transistor to drive
15 a speaker connected between the two current output terminals and
save power.